

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|-------|------------------|---------|------------------|
| L1    | 312  | (controller and register and FIFO).CLM.                     | USPAT | OR               | OFF     | 2006/02/15 11:20 |
| L2    | 3    | (controller and register and FIFO and latched).CLM.         | USPAT | OR               | OFF     | 2006/02/15 11:20 |
| L3    | 163  | (controller and register and latched).CLM.                  | USPAT | OR               | OFF     | 2006/02/15 11:21 |
| L4    | 5578 | (controller and register and latched mathematical).CLM.     | USPAT | OR               | OFF     | 2006/02/15 11:21 |
| L5    | 1    | (controller and register and latched and mathematical).CLM. | USPAT | OR               | OFF     | 2006/02/15 11:21 |
| L6    | 20   | (controller and register and latched and states).CLM.       | USPAT | OR               | OFF     | 2006/02/15 11:22 |
| L7    | 0    | (controller and register and latched and MRI).CLM.          | USPAT | OR               | OFF     | 2006/02/15 11:22 |
| L8    | 0    | (controller and register and latched and imager).CLM.       | USPAT | OR               | OFF     | 2006/02/15 11:23 |
| L9    | 1    | (controller and register and FIFO and imager).CLM.          | USPAT | OR               | OFF     | 2006/02/15 11:23 |